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AUG 22 2002



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Ananda Sarangi, et al.

Application No.: 09/746,168

Filed: December 22, 2000

For: DYNAMIC PROCESSOR CONFIGURATION
AND POWER-UP) Examiner: Lee, T.
) Art Unit: 2815Commissioner for Patents
Washington, D.C. 20231REQUEST TO CORRECT AN ERROR IN THE FILING RECEIPT

Dear Sir:

Applicants respectfully note that the filing date on the official Filing Receipt for the above-identified application is incorrect. That Filing Receipt erroneously lists the filing date as 2/26/2001. The correct filing date is 12-22-2000 (emphasis added).

Therefore, correction with respect to the filing date listed in the official Filing Receipt of the present application is respectfully requested. Please change the filing date of the present application to 12-22-2000. Enclosed is a copy of the Decisions Granting Petitions, copy of the official Filing Receipt, the redlined correction copy of the official Filing Receipt, and a copy of the Express Mail Receipt. Please forward a corrected copy of the official Filing Receipt to the undersigned.

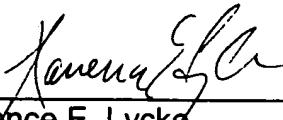
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As these mistakes were made on the part of the Patent and Trademark Office, no further fees are required.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 8/16/02



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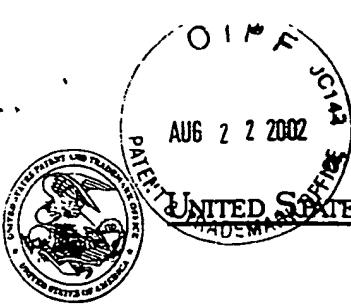
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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY.DOCKET.NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/746,168	02/26/2001	2185	1000	42390.P9470	9	20	5

CONFIRMATION NO. 2325

UPDATED FILING RECEIPT



OC000000007799889

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Jan Carol Little
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Applicant(s)

Ananda Sarangi, Beaverton, OR;
Rachael Jade Parker, Forest Grove, OR;
Edward P. Osburn, Folsom, CA;
Gregory F. Taylor, Portland, OR;

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted 02/06/2001

Projected Publication Date: 08/29/2002

Non-Publication Request: No

Early Publication Request: No

Title

Dynamic processor configuration and power-up

Preliminary Class

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Projected Publication Date: 08/29/2002**Non-Publication Request:** No**Early Publication Request:** No**Title**

Dynamic processor configuration and power-up

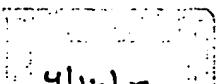
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